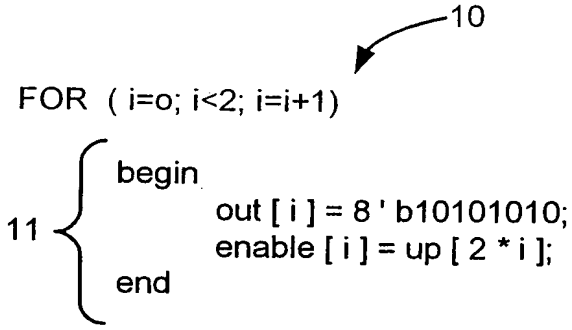

Figure 1

(Prior Art)

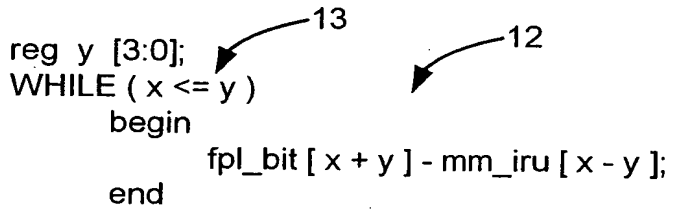

Figure 2

(Prior Art)

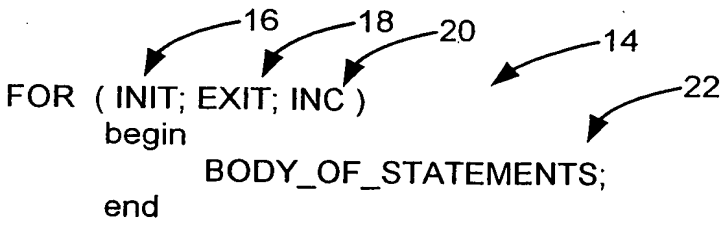

Figure 3

(Prior Art)

```
out [ 0 ] = 8 ' b10101010 ;
enable [ 0 ] = ~up [ 0 ] ;

out [ 0 ] = 8 ' b10101010 ;
enable [ 0 ] = ~up [ 2 ] ;
```

24

Figure 4

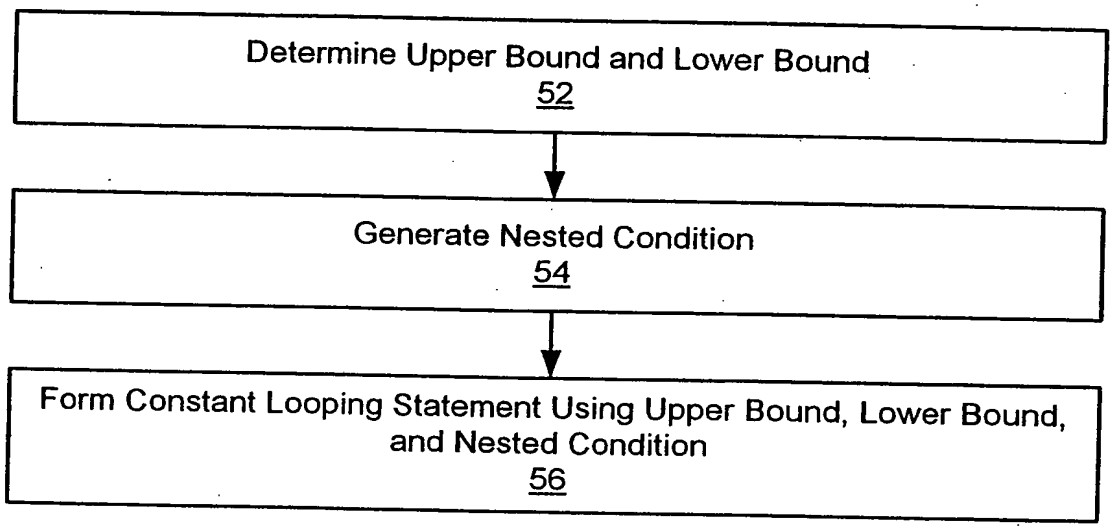

Figure 5

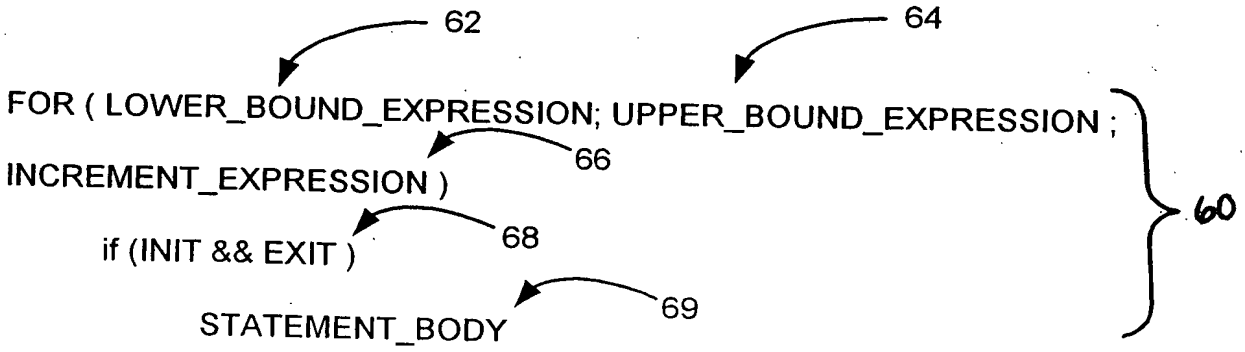

Figure 6